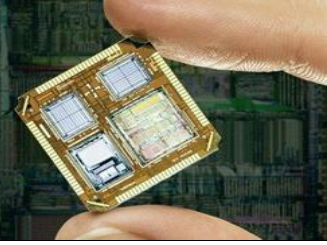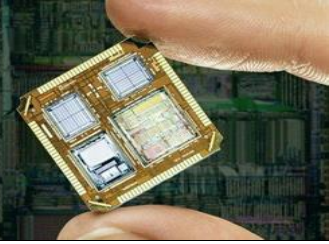# Uniquify™
engineer ai™

Corporate Internship Program

Contents

1. **Uniquify Corporate overview**

2. **Internship program overview**

3. **Internship requirements**

Uniquify

Uniquify Corporate overview

Our DNA…

Uniquify – Engineer AI

Founded in 2005 in Silicon Valley (San Jose, California) by semiconductor industry veterans, Uniquify's vision is to become one of the world's leading high-end System-On-a-Chip(SoC) design and manufacturers cantered around AI technology. It produces UHD/QUHD display processors, SSD controller, and AIoT processor. Uniquify introduced world's first X-Aware Neural-network Platform (XNP), which provides an SoC platform for hardware-based AI computing with higher performance and lower power.

Uniquify employs new design principles and its own platform (XNP) to dramatically increase performance and lower development cost and time-to-market of AI semiconductor products for the visual, mobile, and networking segments..
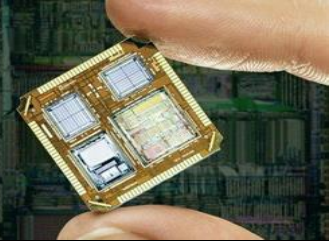

Uniquify Head Office, Silicon Valley in San Jose

Uniquify Korea Branch Office at Korea Design Center, Seongnam-si
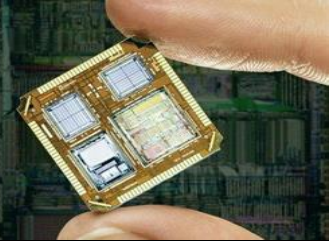
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Worldwide Presence…

Location	Headcount
Santa Clara, California, USA (HQ)	42
Bangalore, India	4
Seoul, Korea	22
Shin Yokohama, Japan	6
HCM City & Danang, Vietnam	47
Total	**121**

Santa Clara
USA (HQ)

Seoul
Korea

Shin-Yokohama
Japan

Bangalore
India

HCM & Danang
Vietnam

Uniquify

Internship program overview

Internship period

6 months to 12 months in US or Korea.

Divisions for Recruitment

- SoC Design
- Algorithms
- AI

Job Description

SoC Design
Design and verify logic, systems, and algorithms to meet product requirements.
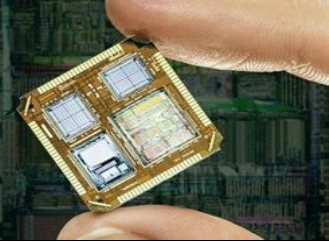
Algorithms
Image processing algorithm survey and development, FPGA implementation and verification. Image quality evaluation, measure, and parameter tuning.

AI
Great chance to learn AI/ML, Implement your knowledge of image DSP and/or AI/ML to design the image analysis and feature recognition algorithms, Use your coding expertise to turn your algorithm to efficient, cross-platform portable code which will run on a variety of different mobile and server platforms.

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Internship requirements

SoC Design
- Ability to handle tasks with precision and accuracy
- Perseverance and integrity to solve problems
- Computational algorithm design and implementation in Verilog
- Knowledge of Perl, UNIX shell, or equivalent scripting languages
- Creating/updating/maintaining fully-self-checking test benches using behavioral Verilog
- Performing synthesis and/or static timing analysis on complex designs
- Standards to Specification Translation, Verification methodologies (such as OVM, UVM and VMM) and DFT experience is a plus

Algorithms
- Knowledge of C/C++, MATLAB, Python
- Experience with FPGA implementation
- Experience with omage processing

AI
- Preferred major is Computer Science, Electrical engineering, or a related field
- Excellent coding skills
- Experience with Python and Unix
- Working knowledge of various machine learning frameworks (e.g. Tensorflow, PyTorch, scikit-learn) is a big plus
- Great communication and team work skills, detail-oriented, efficient learners

Uniquify