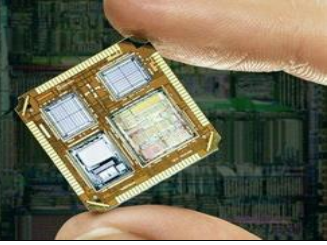




UniquifyTM
engineer **ai**TM

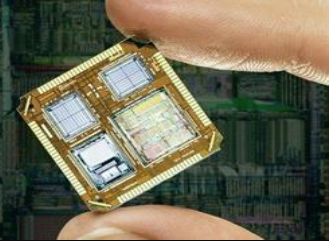
Corporate Internship Program

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Uniquify Corporate overview



Who is Uniquify?

Uniquify – Engineer AI

Founded in 2005 in Silicon Valley (San Jose, California) by semiconductor industry veterans, Uniquify's vision is to become one of the world's leading high-end System-On-a-Chip(SoC) design and fabless manufacturing businesses. It produces its own product targeting ultra-high definition displays for Digital TV, and related consumer display products. It offers SoC design, integration and manufacturing services to leading semiconductor and system companies worldwide, in silicon processes down to 14nm, with a portfolio of market-leading, silicon-proven DDR memory interface IP.

Uniquify employs new design principles and software algorithms to dramatically increase performance and lower development cost and time-to-market of semiconductor products for the visual, mobile, and networking segments. This gives Uniquify a competitive advantage in the fabless semiconductor marketplace.

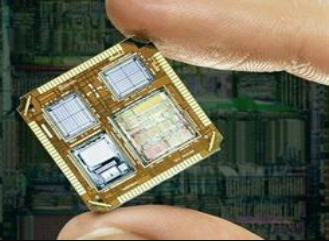


Uniquify Head Office, Silicon Valley in San Jose



Uniquify Korea Branch Office at Korea Design Center, Seongnam-si

Uniquify Corporate overview



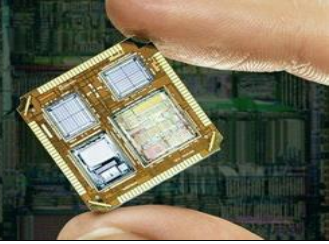
Uniquify is among the most experienced SoC design, planning, and execution companies with hundreds of successfully completed designs for its customers. Further, its innovative design technologies and semiconductor intellectual properties have set it apart in the marketplace. Uniquify has created Perseus, its own proprietary development, planning and implementation software platform for SoCs based on an internal next-generation architecture. Uniquify's design teams use Perseus to dramatically lower development cost by improving the productivity of the engineering team and delivering higher-quality and more robust designs. This directly impacts the business of larger electronics system companies by improving the feature set and performance of their products, while at the same time reducing end-product cost, and getting premium products to market faster.

The innovative technology in Perseus combined with its experienced design team has made Uniquify a “go-to” SoC design and implementation partner for some of the world's largest and most important electronics companies. These and many other companies use Uniquify as an SoC partner to provide specialized SoC chips and semiconductor intellectual property (IP).

Uniquify's fab partners include the leading semiconductor foundries: GlobalFoundries, Samsung, SMIC, TSMC and UMC. We also partner with design IP and implementation companies such as Brite Semiconductor, Invecus, and Silicon Creations to create high performance and cost-effective solutions for our customers.

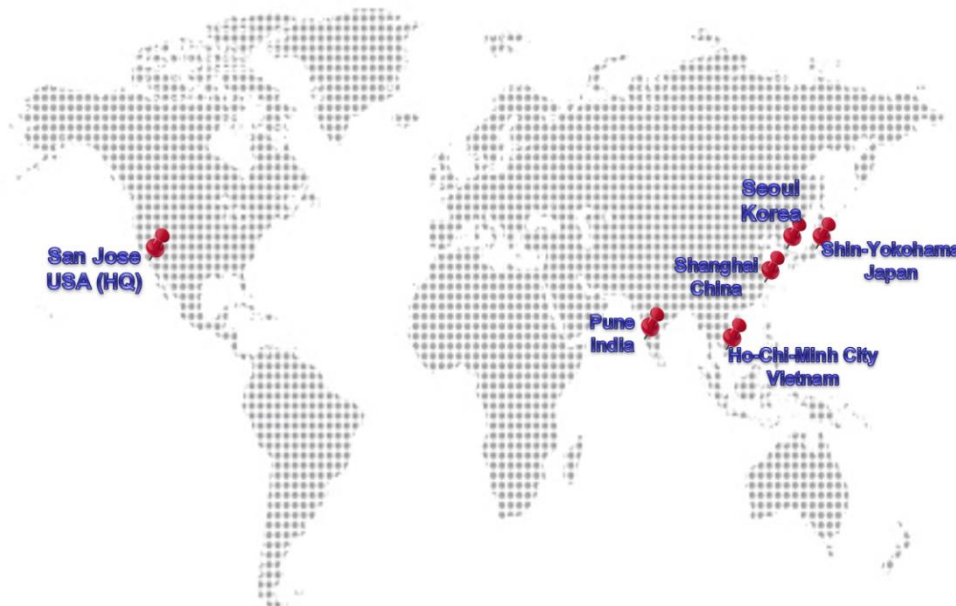


Uniquify Corporate overview



- Self-funded / Organic growth for 14+ years (No Outside Investment)
- IP BU owns 18 issued / 2 pending US patents in high speed interface technologies
- UHD (4K) panel Display Processor in mass production
- Industry's first AI enabled Display Processor for QUHD(8K) panel market
 - Production scheduled in 2020

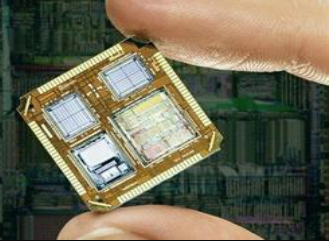
Uniquify Worldwide



LOCATION	HEADCOUNT
San Jose, California, USA (HQ)	32
Bangalore, India	7
Seoul, Korea	22
Shin Yokohama, Japan	6
Shanghai, China	3
Ho Chi Minh City, Vietnam	24
Danang, Vietnam	11
TOTAL	105+

Uniquify has branch offices world-wide. Including HQ, we have 7 offices and 100+ trained engineers in the world.

Uniquify Corporate overview



Our Business

SoC Services

Uniquify offers a range of SoC Services at any point of the IC design process from project architecture through to implementation, packaging and test. For your existing IP we can provide hardening and customization services. And our Fables manufacturing capabilities reflect our close relationships with leading semiconductor foundries such as GF, Samsung, SMIC and TSMC.

SoC PLANNING

Architecture
Specification
IP Qualification &
Selection

LOGIC DESIGN

RTL Design
FPGA Prototyping
Logic Verification
Static Timing
Analysis
Board Design

DFT

DFT Insertion
DFT Verification

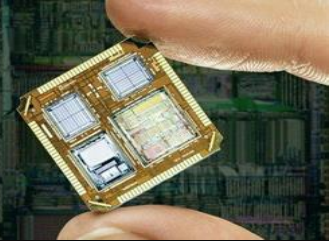
PHYSICAL DESIGN

Logic Synthesis
Floorplanning
Chip-Level Planning
Place & Route
DRC/LVS/ANT/ERC
Parasitic Extraction
Power/IR-Drop
Analysis
STA/SI Analysis

MFRG. SERVICES

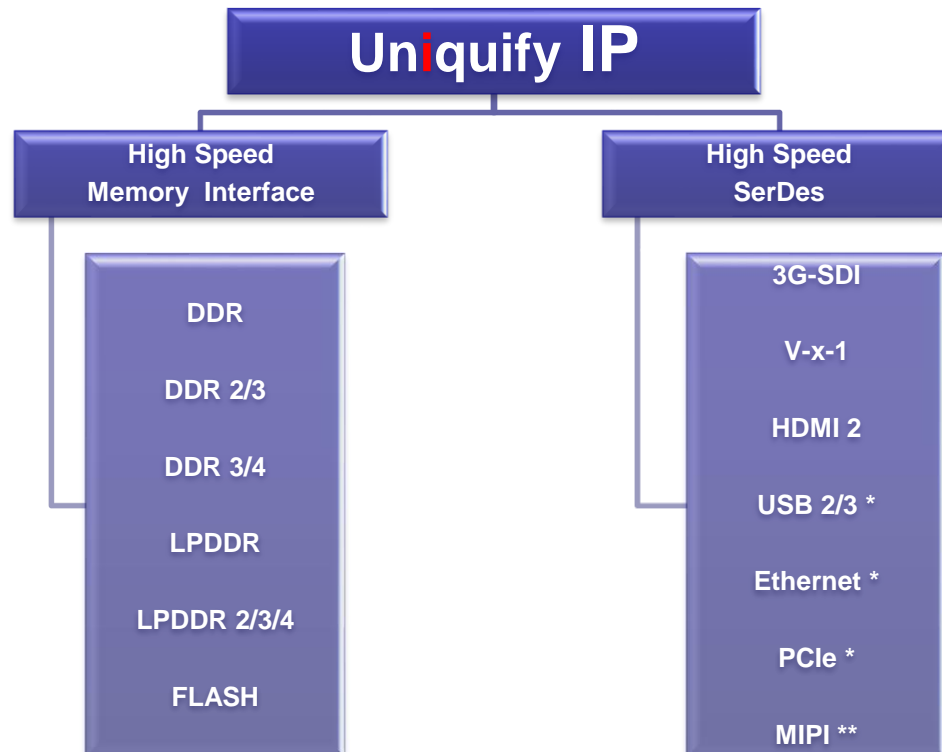
Package Design
Package Simulation
SI/PI Analysis
Foundry/Supply Chain
Management
Product Qualification
SoC Product Testing

Uniquify Corporate overview

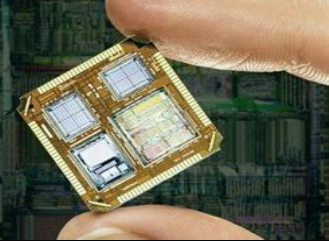


Design IP

Uniquify offers a complete suite of Adaptive IP that delivers the highest performance, lowest-power and area efficient in silicon process nodes down to 14nm. Select one of the IP categories on this page to see more information



Internship program overview



Internship period

6 months to 12 months in US.

Divisions for Recruitment

- SoC Design
- Algorithms
- AI

Job Description

SoC Design

Design and verify logic, systems, and algorithms to meet product requirements.

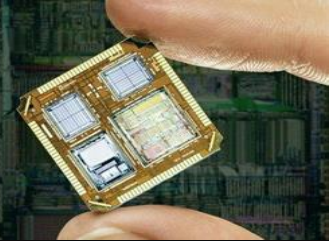
Algorithms

Image processing algorithm survey and development, FPGA implementation and verification. Image quality evaluation, measure, and parameter tuning.

AI

Great chance to learn AI/ML, Implement your knowledge of image DSP and/or AI/ML to design the image analysis and feature recognition algorithms, Use your coding expertise to turn your algorithm to efficient, cross-platform portable code which will run on a variety of different mobile and server platforms.

Internship requirements



SoC Design

- Ability to handle tasks with precision and accuracy
- Perseverance and integrity to solve problems
- Computational algorithm design and implementation in Verilog
- Knowledge of Perl, UNIX shell, or equivalent scripting languages
- Creating/updating/maintaining fully-self-checking test benches using behavioral Verilog
- Performing synthesis and/or static timing analysis on complex designs
- Standards to Specification Translation, Verification methodologies (such as OVM, UVM and VMM) and DFT experience is a plus

Algorithms

- Knowledge of C/C++, MATLAB, Python
- Experience with FPGA implementation
- Experience with image processing

AI

- Completed BS/MS degree in Computer Science, Electrical engineering, or a related field
- Excellent coding skills
- Experience with Python and Unix
- Working knowledge of various machine learning frameworks (e.g. Tensorflow, PyTorch, scikit-learn) is a big plus
- Great communication and team work skills, detail-oriented, efficient learners